

### **Amendments to the Claims:**

Re-write the claims as set forth below. This listing of claims will replace all prior versions and listings, of claims in the application:

### **Listing of Claims:**

1. (Previously presented) A video driver system comprising:

a first graphics device having an input and a first video component output to provide a first video output component signal;

a second graphics device having an input and a first video component output to provide a first video output component signal;

a first video output port coupled to the first video component output of the first graphics device and the first video component output of the second graphics device; and

a second video output port coupled to the first video component output of the second graphics device.

2. – 17. (Canceled)

18. (Previously presented) The system of claim 1 further comprising a monitor coupled to the first video output port.

19. (Previously presented) A method of providing a video signal, the method comprising:

generating a first signal at a first device, wherein the first signal is representative of a first video output component;

providing the first signal to a first node;

determining a value of the first signal at a first output node;

generating a second signal at a second device, wherein the second signal is representative of a first video output component;

providing the second signal of the second device to the first output node; and

adjusting the second device until a value of the second signal at the first output node substantially matches the determined value of the first signal at the first output node.

20. (Previously presented) The method of claim 19, further comprising the step of:  
removing the first signal from the first node prior to the step of providing the second signal.

21. (Previously presented) The method of claim 19, wherein the value of the first and second signals is a voltage value.

22. (Currently amended) The method of claim 19, wherein the step of determining includes ~~the substep of:~~

modifying and comparing the value of the first ~~[[device]]~~signal until the value of the first signal substantially matches a predetermined value.

23. – 28. (Canceled)

29. (Previously presented) A video driver system comprising:

a first graphics device having an input and a first video component output to provide a first video output component signal;

a second graphics device having an input and a first video component output to provide a first video output component signal;

a first video output port coupled to the first video component output of the first graphics device and to the first video component output of the second graphics device; and

a second video output port coupled to the first video component output of the second graphics device;

wherein the first graphics device renders a frame of video and provides the rendered frame to the first video output port, and

wherein the second graphics device renders an adjacent frame of video and provides the adjacent frame to the first video output port.

30. (Currently amended) An apparatus for providing video signals comprising:

a first graphics device operative to render a first frame of video;

a second graphics device operative to render a second frame of video; and

a common port, operatively coupled to receive the first and second frames of ~~video~~ of rendered video from either of the first and second graphics devices.

31. (Previously presented) The apparatus of claim 30 including a first frame buffer operatively coupled to the first graphics device and a second frame buffer operatively coupled to the second graphics device.

32. (Previously presented) The apparatus of claim 31 including at least one digital to analog converter operatively coupled to output video and having voltage adjusted in order to correlate video out voltages being provided by at least one of the graphics devices.

33. (Previously presented) The apparatus of claim 30 including circuitry operative to provide digital to analog conversion voltage equalization associated with the first and second graphics devices.

34. (Previously presented) The apparatus of claim 30 wherein the first graphics device includes a controller operative to select video from the second graphics device to be output to the common port.

35. (Previously presented) The apparatus of claim 30 including a load operatively couplable to either one of first and second graphics devices when at least one of the first and second graphics devices is not driving the common port.

36. (Previously presented) The apparatus of claim 30 wherein the first graphics device acts as a master to the second graphics device and provides synchronization control for the second graphics device.

37. (Previously presented) The apparatus of claim 36 wherein the first graphics device includes a reference signal generator for the second graphics controller.

38. (Previously presented) The apparatus of claim 30 wherein the first graphics device and second graphics devices are video graphics adapters.

39. (Previously presented) The apparatus of claim 30 wherein the first and second rendered frames are adjacent frames of video.